

Notice of References Cited

Application/Control No.

10/749,694

Applicant(s)/Patent Under
Reexamination
GORINEVSKY, DIMITRY

Examiner

Nathan Bloom

Art Unit

2624

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U.S. PATENT DOCUMENTS

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| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|---|
| | U | Hosagrahar V. Jagadish et al., "Array Architectures for Iterative Algorithms", Proceedings of the IEEE, Vol. 75, NO. 9, Sept. 1987 |
| | V | J. Greg Nash et al., "VLSI Implementation of a Linear Systolic Array", Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '85, Vol. 10, 1985 |
| | W | S.A. Amin et al., Acoustics, "PVM Implementations for Low-level Image Processing Systolic Array Designs", Proceedings of the EuroPVM95 Conference, Lyon, France, May 1995 |
| | X | Richard Hughey, "Programming Systolic Arrays", IEEE Computer Society, Int. Conf. Proceedings Application-Specific Array Processors Aug. 4-7 1992 |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.